

High Sensitivity 10Gb/s Si Photonic Receivers based on a Low-Voltage Waveguide-coupled Ge Avalanche Photodetector

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Abstract: We demonstrate low-voltage germanium waveguide avalanche photodetectors (APDs) with a gain×bandwidth product above 100GHz. A photonic receiver based on such a Ge APD, including a 0.13μm SiGe BiCMOS low-noise trans-impedance amplifier and a limiting amplifier, is realized. A 5.8dB sensitivity improvement is demonstrated at -5.9V bias at an avalanche gain of 6 through bit error ratio measurements. The absolute sensitivity in avalanche mode is -23.4dBm and -24.4dBm at a bit error ratio of 1×10^{-12} and 1×10^{-9} respectively.

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1. Introduction

Avalanche photodetectors (APDs) integrated in a silicon photonics platform offer great potential to improve the power budget of Si-based optical interconnects. Moreover, they are potentially disruptive in passive optical access networks requiring high sensitivity at moderate bitrates (10Gbit/s) and low cost. By leveraging the internal gain of the APD, optical receivers can be realized with significantly improved optical sensitivity as compared to conventional PIN photodetector receivers [1]. Multiplication gain and excess noise are key performance metrics for APDs. Although Ge APDs can achieve high gain using carrier multiplication close to avalanche breakdown [2,3], they are generally considered to suffer from the high multiplication noise in germanium [4]. High avalanche gain with low excess noise has been demonstrated in surface normal operating Ge detectors in a Separate Absorption, Charge, and Multiplication (SACM) configuration using Ge as light absorption layer and Si as the multiplication layer [5], enabling an impressive gain-bandwidth product of 340GHz and -28dBm receiver sensitivity at 10Gb/s. However, this implementation requires a high bias voltage of around -25V, which is not compatible with CMOS supply voltages. High avalanche gain at low voltage (-3V) was also reported [6] in waveguide APDs comprised of a thin Ge layer with metal-semiconductor-metal (MSM) contacts. Strongly non-uniform electric fields generated by the interdigitated contacts were exploited to mitigate the intrinsically poor avalanche excess-noise properties of bulk Ge. However, the large dark current of the MSM device and poor primary responsivity strongly limited the receiver sensitivity. Recently, a lateral PIN junction based Ge waveguide APD with gain larger than 10 at a bias voltage of -7V was reported in [7]. The device shows a low dark current of 18nA at -1V. However, [7] neither demonstrates the sensitivity improvement through bit error rate measurements nor characterizes the avalanche excess noise performance of the device.

In this paper, we demonstrate a vertical PIN junction Ge waveguide APD integrated on the imec 200mm Si photonics platform. By engineering a thin Ge multiplication layer in a vertical PIN structure, a 3dB opto-electrical bandwidth above 10GHz at an avalanche gain of 10.2 is obtained at -6.2V, resulting in a gain×bandwidth product (GBP) above 100GHz. The device has a low dark current of 17nA at -1V and a high primary responsivity of 0.6A/W. The Ge APD is wire-bonded to a 0.13μm SiGe BiCMOS low-noise trans-impedance amplifier (TIA), and bit error ratio (BER) measurements are implemented on the wire-bonded optical

receiver. A significant sensitivity improvement of 5.8dB at -5.9V is demonstrated. The primary sensitivity is -17.6dBm and -18.6dBm average optical power (non-return-to-zero modulation at 10Gb/s) at -1.7V for a bit error ratio of 10^{-12} and 10^{-9} respectively. This results in an absolute sensitivity in avalanche mode of -23.4dBm and -24.4dBm at a bit error ratio of 10^{-12} and 10^{-9} respectively.

2. Device structure and fabrication process

The Ge waveguide APDs are implemented in imec's fully integrated Si Photonics Platform along with Si modulators [8] and various passive devices [9]. They go through a process flow described in [10]. The cross-sectional dimensions of the Ge APD are shown in Fig. 1(a). Fig. 1(b) shows a TEM image of the Ge APD's longitudinal cross section. The spacing between the p-contact plugs is $1.2\mu\text{m}$. With phosphorus ion implantation in silicon before Ge epitaxy and boron ion implantation in the planarized Ge layer, a vertical $\text{P}^+\text{-I-N}^+$ (VPIN) diode is formed. The simulated doping distribution (Monte Carlo ion implantation simulation) in the Ge layer is shown in Fig. 1(c). This heterogeneous Ge/Si VPIN diode configuration results in a strong electric field as high as $1 \times 10^5 \text{ V}\cdot\text{cm}^{-1}$ confined in the lower 200nm of the Ge layer at -5.5V bias voltage, as shown in Fig. 1(d). Hence, it is expected that strong avalanche multiplication can take place at moderate applied bias voltage, and that part of the avalanche excess-noise generation can be suppressed [6, 11-13].

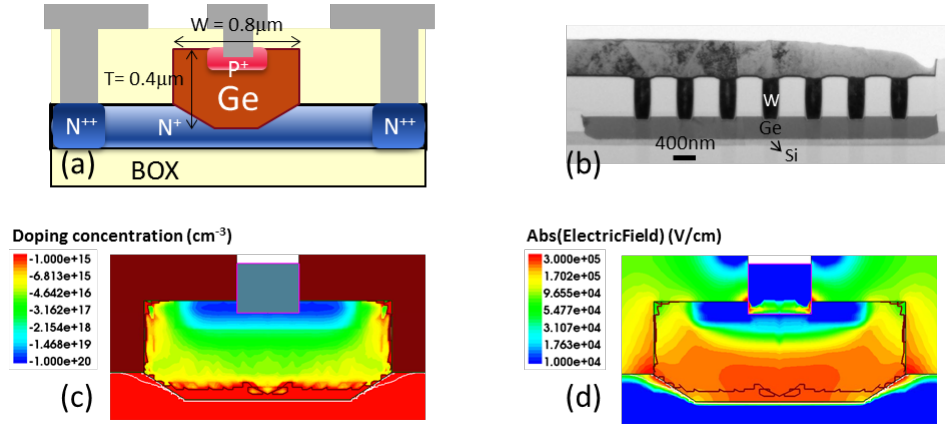


Figure 1. (a) Schematic cross section of the Ge waveguide APD with the Ge layer dimensions. (b) TEM longitudinal cross-section image. (c) Doping distribution in the Ge layer generated from Monte-Carlo ion implantation simulation. (d) Simulated electric field distribution in the Ge layer at -5V applied bias voltage.

3. Standalone APD characteristics

3.1 Static measurements

A typical static current-voltage characteristic of a $14\mu\text{m}$ -long VPIN Ge APD device is shown in Fig. 2(a). The device has a low dark current of 17nA at -1V. As the bias voltage is increased to -4V, the dark current starts to increase rapidly. The breakdown voltage is -6.2V. The light current is measured at 1550nm wavelength with an input optical power of -19.6dBm received by the germanium photodiode. The responsivity is constant from 0V to -3V, owing to the relatively large built-in electrical field that is capable of sweeping out the majority of the photo-generated carriers even at 0V bias. The measured primary responsivity is 0.6A/W. The light current is $7\mu\text{A}$ at -1V. It starts to rise from -4V, and it reaches $260\mu\text{A}$ at -6.2V.

The avalanche gain extracted from these static measurements, defined as the multiplication factor of the net-light current ($=$ light current $-$ dark current) is shown in Fig. 2(b). The gain increases sharply as the bias voltage becomes larger than -5V , reaching its maximum value at -6.2V . Beyond this breakdown voltage, the gain decreases instead. It can be seen that at 90%, 95% and 98% of the breakdown voltage, the avalanche gain is 3.5, 6.3 and 10.0 respectively.

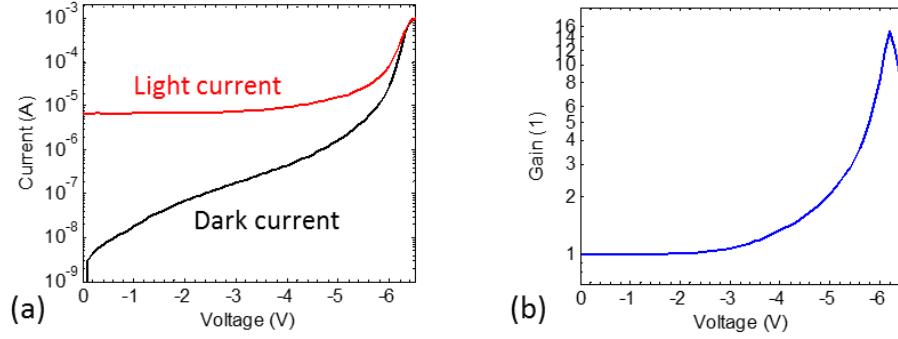


Figure 2. (a) I-V characteristics of a $14\mu\text{m}$ -long Ge APD. (b) Avalanche gain extracted from the static measurements.

3.2 Small-signal measurements

Next, small-signal radio-frequency (RF) measurements at 1550nm wavelength are carried out. Using an average input optical power of -15.8dBm , the RF power delivered by the photodetector to a vector network analyzer (the S_{21} parameter) is recorded as a function of frequency for various applied bias voltages, as shown in Fig. 3. It can be seen that the low-frequency RF power increases with bias voltage until -6.2V . Beyond this voltage, the low-frequency RF power drops instead, in accordance with the static measurements. Also, it can be seen that the 3dB opto-electrical bandwidth drops substantially with increasing bias voltage.

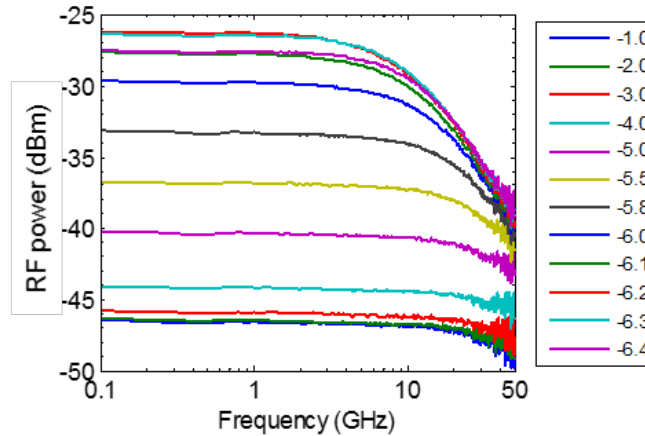


Figure 3. Small-signal RF measurement of S_{21} parameter for various bias voltages.

Avalanche gain extracted from small-signal measurements, defined as the square root of the multiplication factor of the low-frequency RF power (using the low frequency RF power at -1V as a reference), is shown in Fig. 4(a). Similar to what was obtained from the static measurements, the avalanche gain extracted from small-signal measurements reaches its maximum value at -6.2V. The 3dB opto-electrical bandwidth versus avalanche gain is shown in Fig. 4(b). At low bias voltages, the 3dB bandwidth is as high as 50GHz (limited by the measurement setup). It decreases slowly as long as the multiplication gain is smaller than 2. As the gain further increases, the 3dB bandwidth drops almost inversely proportional to the avalanche gain owing to the avalanche build-up time [14,15]. At -6.2V APD bias, a 3dB bandwidth of 10.4GHz at the avalanche gain of 10.2 is obtained. The gain \times bandwidth product (GBP) is shown in Fig. 3(c). It can be seen that the GBP reaches a plateau at -5.8V (\sim 100GHz), after which it further increases due to bandwidth enhancement as seen in Fig. 3(b), similar to what is reported in [16]. The 100GHz GBP is comparable to standard InP-based APDs [5].

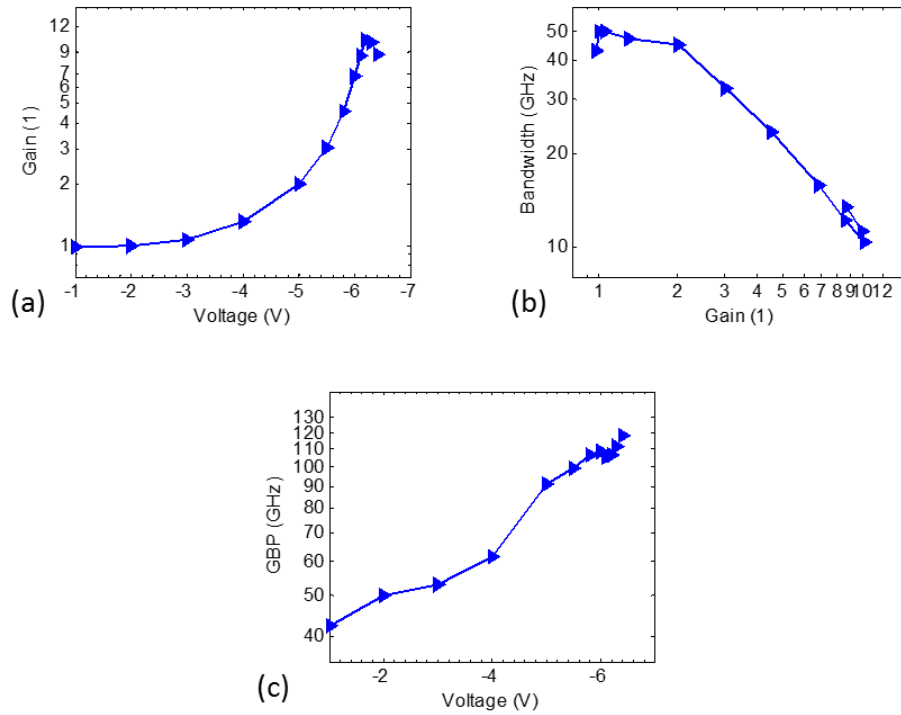


Figure 4. (a) Avalanche gain extracted from small-signal RF measurements as a function of bias voltage. (b) Measured 3dB opto-electrical bandwidth versus avalanche gain extracted from the S_{21} RF curves. (c) gain \times bandwidth product as a function of bias voltage.

3.3 Avalanche excess noise characteristics

Next, excess multiplication noise measurements are performed to characterize the avalanche noise performance of the Ge APDs. The power spectral density (PSD) of the noise current at 150MHz in both dark current and light current were measured using a low-noise signal analyzer. The power spectral density under consideration (the net-light current power spectral density) is given by equation (1) in the avalanche multiplication regime,

$$PSD = 2 \times q \times I \times M^2 \times F(M) \quad (1)$$

where I is net-light current (= light current – dark current) and M is net-light current gain (the avalanche gain extracted from static measurements). q is the elementary charge. The excess noise factor $F(M)$ deduced from equation (1) as a function of gain is shown in Fig. 5(a) and Fig. 5(b) at 1550nm wavelength for an input optical power of -23.8dBm and -18.8dBm, respectively. The excess noise factor can be expressed for the case of avalanche multiplication in a uniform electric field when electrons initiate the multiplication as in equation (2),

$$F(M) = k_{eff}M + (2 - 1/M) \times (1 - k_{eff}) \quad (2)$$

where k_{eff} is the effective ratio of ionization coefficients for electrons and holes. These ionization coefficients are almost equal in bulk Ge giving a k_{eff} of about 0.9, which results in a very large excess noise, making conventional Ge APDs uncompetitive for building digital optical links. Fitting the data with equation (2) reveals a k_{eff} of 0.5 in the presented device. The total reduction of the power spectral density of the noise current in the presented device compared to a bulk Ge APD can be estimated as 35% for an avalanche gain of 10. This is attributed to the dead space effect in the thin avalanche multiplication region [6, 11-13].

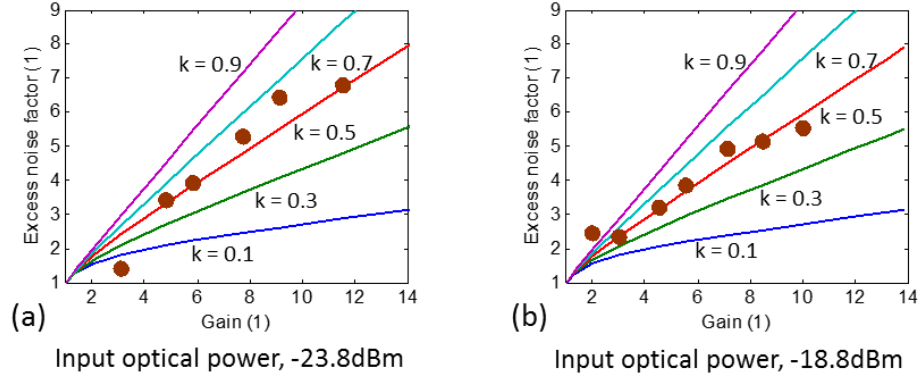


Figure 5. The excess noise factor as a function of gain with an input optical power of (a) -23.8dBm and (b) -18.8dBm.

4. APD receiver characteristics

Finally, in order to assess the sensitivity improvement by operating the APD in avalanche mode, the device was wire-bonded to a 10Gb/s trans-impedance amplifier (TIA), as shown in Fig. 6(a). The TIA is implemented in 0.13 μ m SiGe BiCMOS technology and has a differential output [17,18]. It is designed for burst-mode operation in access networks with an input referred RMS noise current lower than 1.2 μ A. A ($2^{31}-1$) long optical non-return-to-zero pseudo-random bit sequence (PRBS) data pattern at 10Gb/s, generated by a commercial optical modulator with 8.9dB extinction ratio, was launched into the wire-bonded APD receiver. The eye diagram of the optical signal generated by the modulator is shown in Fig. 6(b). A commercial limiting amplifier (LA) is connected to the TIA, and the LA differential outputs (both the DATA and XDATA port) were fed to a 10Gb/s error detector for bit error ratio (BER) measurement. The measured differential BER as a function of input optical power for various bias voltages are shown in Fig. 7. For a BER of 1×10^{-12} (1×10^{-9}), the waveguide-referred primary sensitivity is -17.6dBm (-18.6dBm) average optical power at -1.7V bias voltage, mostly limited by the TIA input-referred noise (RMS) current. The sensitivity increases with increasing bias. At -5.9V APD bias voltage, a 5.8dB sensitivity improvement is obtained, which yields an absolute receiver sensitivity of -23.4dBm and -24.4dBm for a 1×10^{-12} (1×10^{-9}) BER.

10^{-12} and 1×10^{-9} BER respectively. The avalanche gain, extracted from the small-signal measurements, is about 6 at -5.9V bias voltage. Beyond -5.9V, while the gain still rises as the bias voltage increases until -6.2V, the sensitivity saturates due to the excess multiplication noise. 10Gb/s eye diagrams of the electrical signals from the LA with differential BER of $\sim 1 \times 10^{-12}$ at -5.9V bias voltages for both the DATA and XDATA port were recorded by a high-speed oscilloscope, as shown in Fig. 6(c) and Fig. 6(d) respectively.

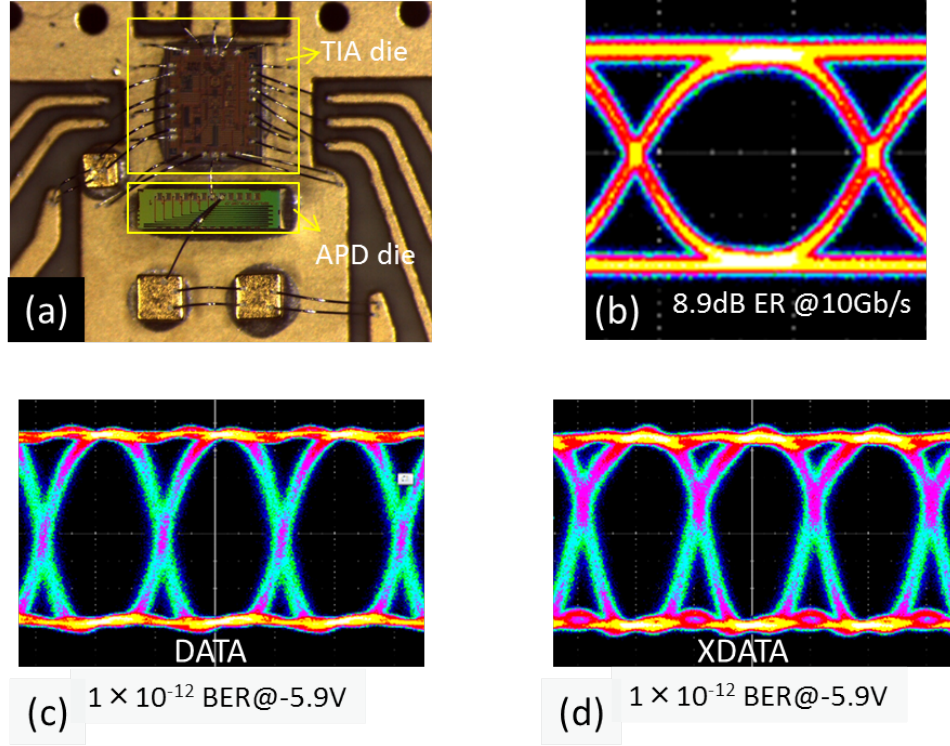


Figure 6. (a) Optical receiver with Ge APD wire-bonded to a TIA. (b) 10Gb/s optical eye diagram from modulator. (c) 10Gb/s eye diagram of the electrical signal from LA DATA port with differential BER of 1×10^{-12} at -5.9V bias voltage (input optical power is -23.4dBm). (d) 10Gb/s eye diagram of the electrical signal from LA XDATA port with differential BER of 1×10^{-12} at -5.9V bias voltage (input optical power is -23.4dBm).

The sensitivity improvement of 5.8dB is lower than that reported in [19], where the Q factor is used to characterize sensitivity and a 7dB improvement is obtained for a Q factor of 7. This is because the low-noise TIA used in this paper has a much smaller input referred RMS noise current than that of the TIA (implemented in 40nm LP CMOS technology) wire-bonded in [19].

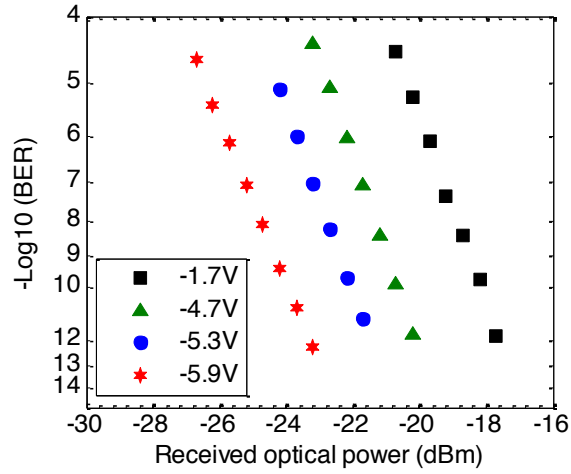


Figure 7. Measured bit error ratio as a function of input optical power for various bias voltages.

5. Conclusion

Low-voltage germanium waveguide APDs are demonstrated with a gain \times bandwidth product over 100GHz. The optical receiver based on such a Ge APD demonstrates a 5.8dB sensitivity improvement. This can compensate for certain channel insertion loss of optical data links, and thus help to satisfy the required link power budget.

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