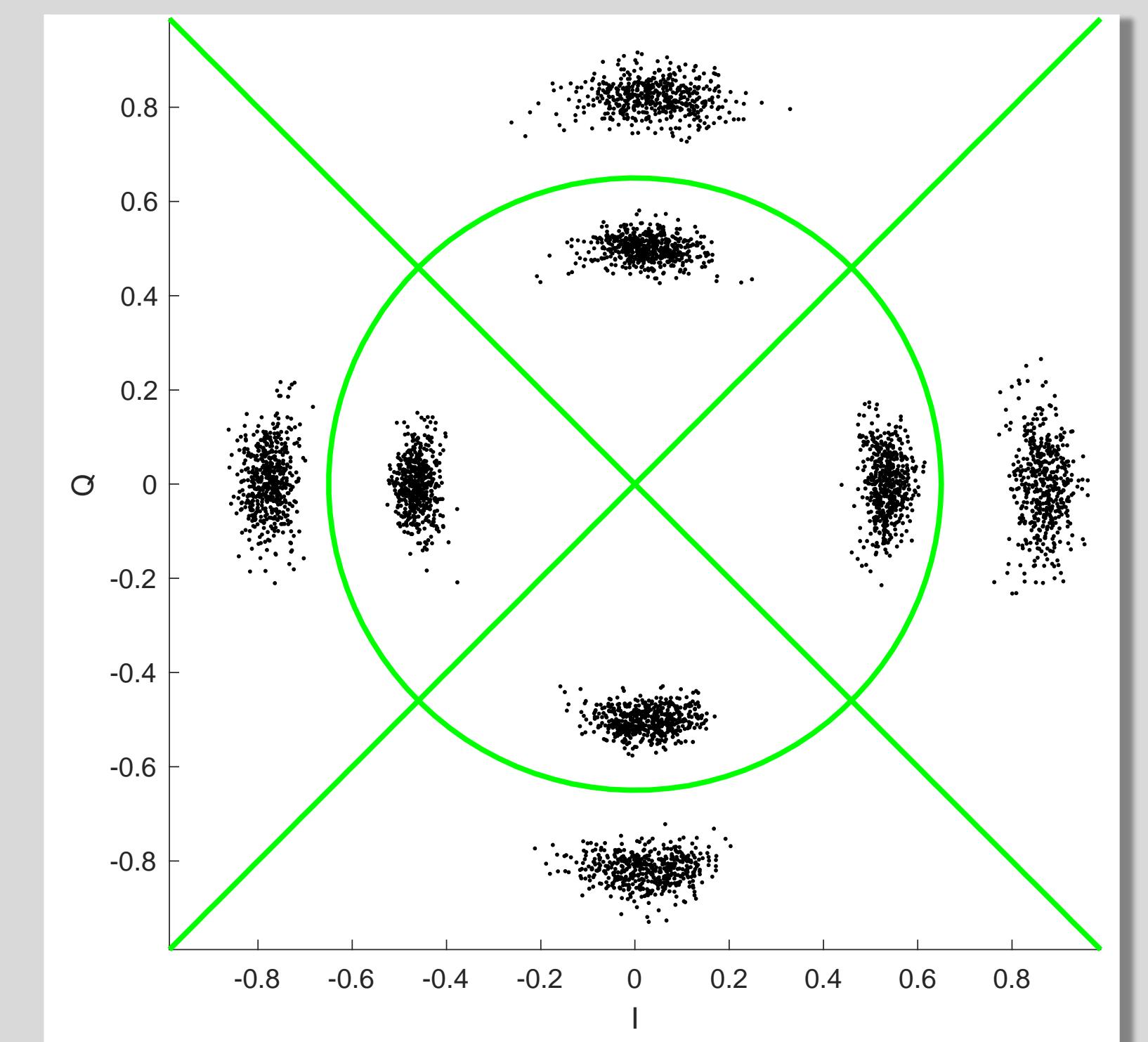
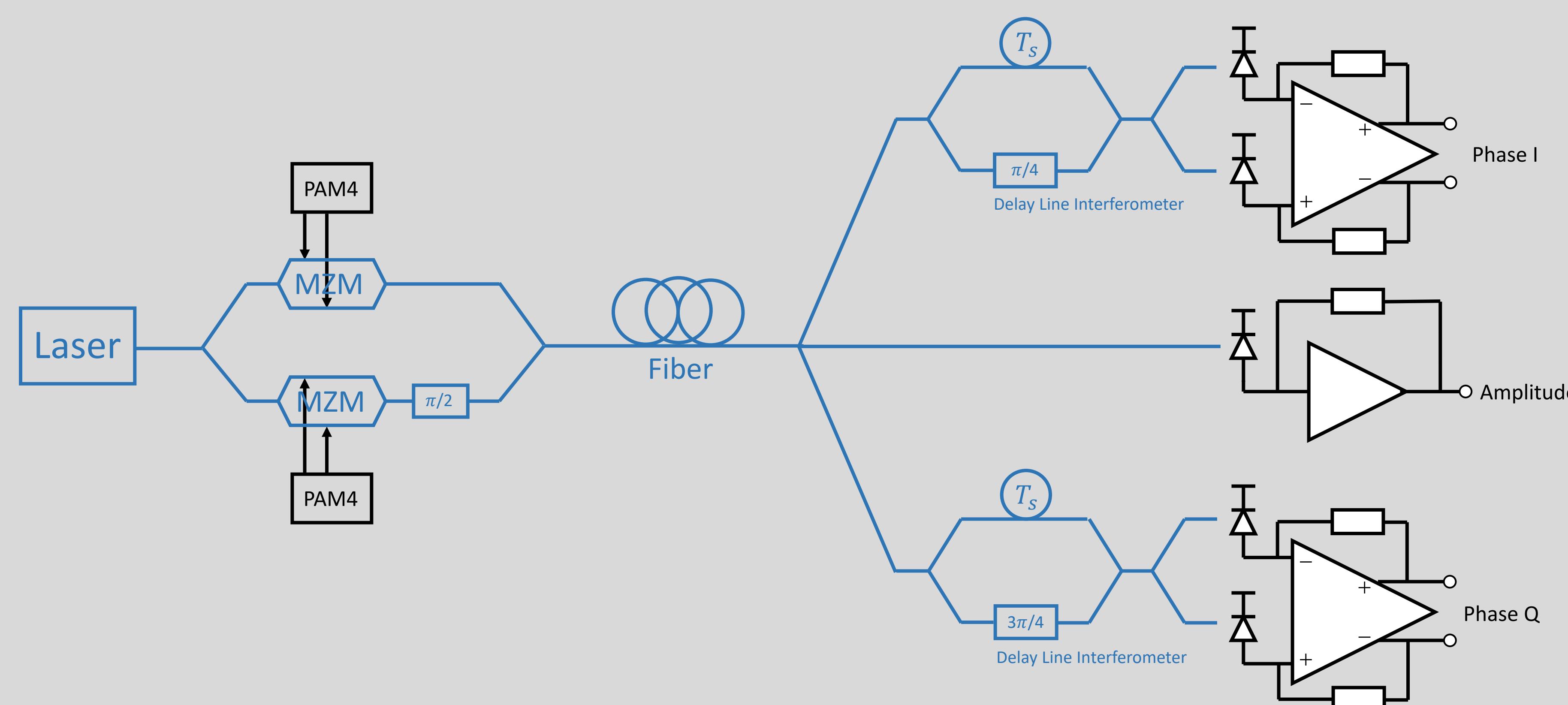


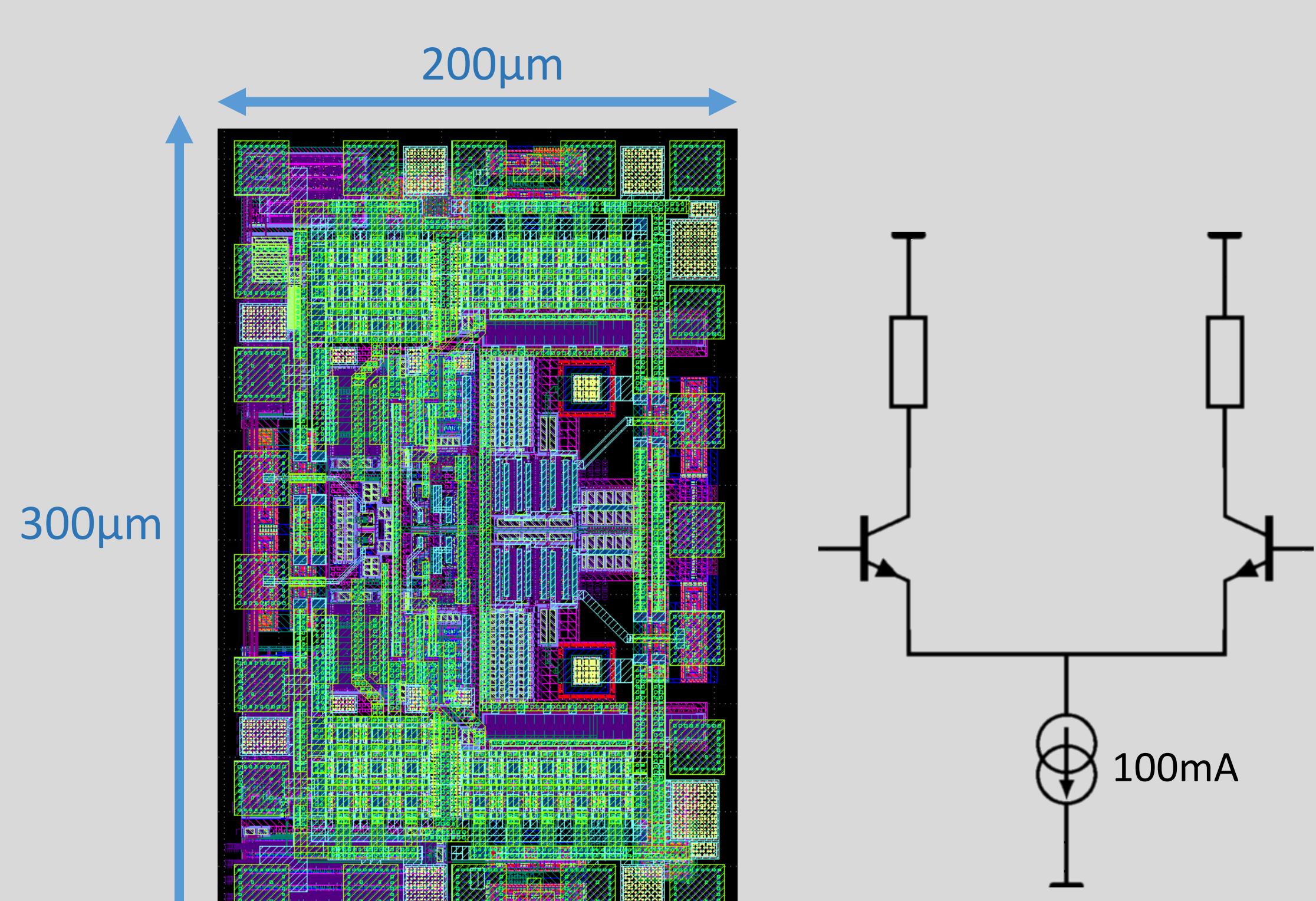
High-Speed Optical Transceivers for Data Centers



ASK-DPSK: Coherent link without LO

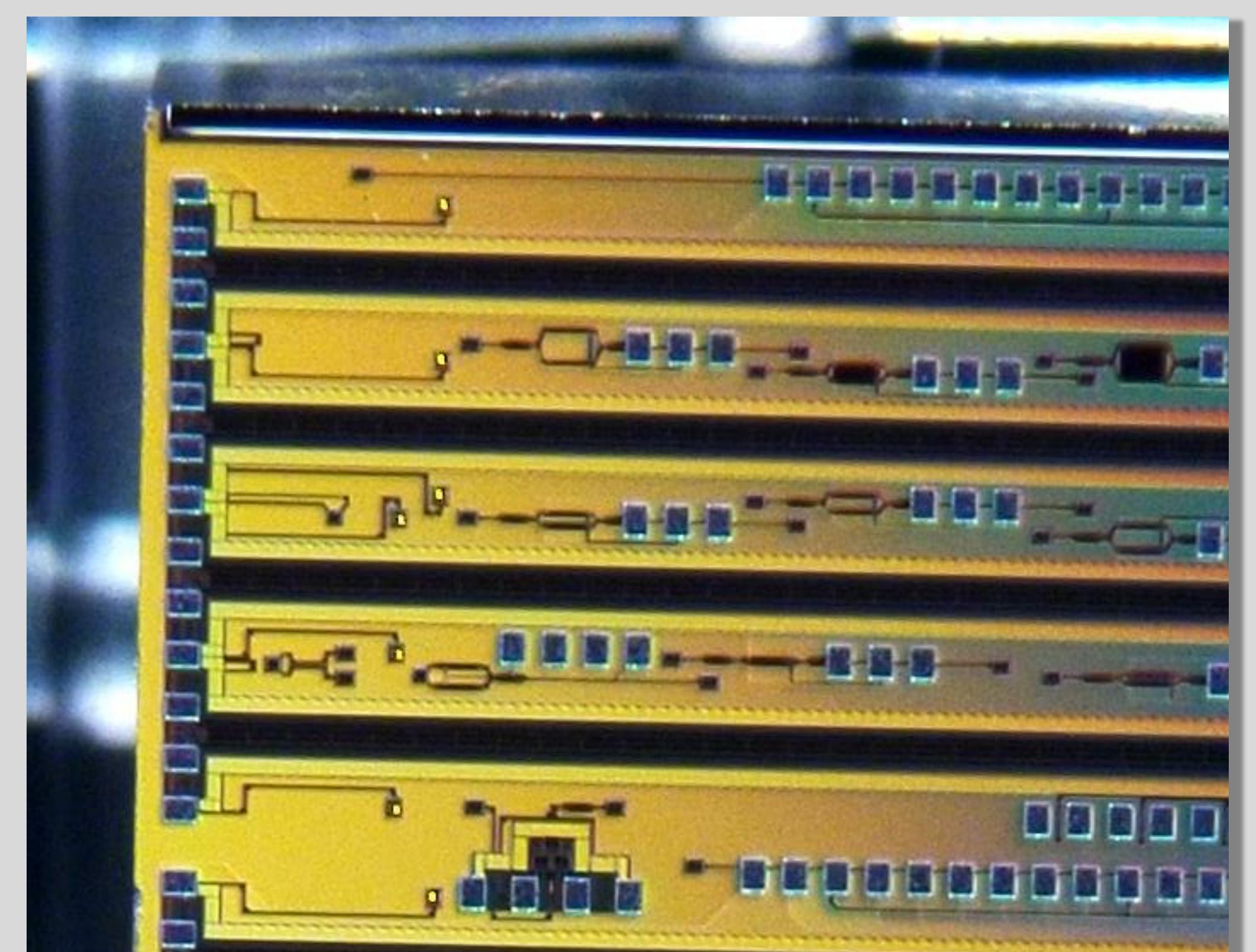


Electronic IC design

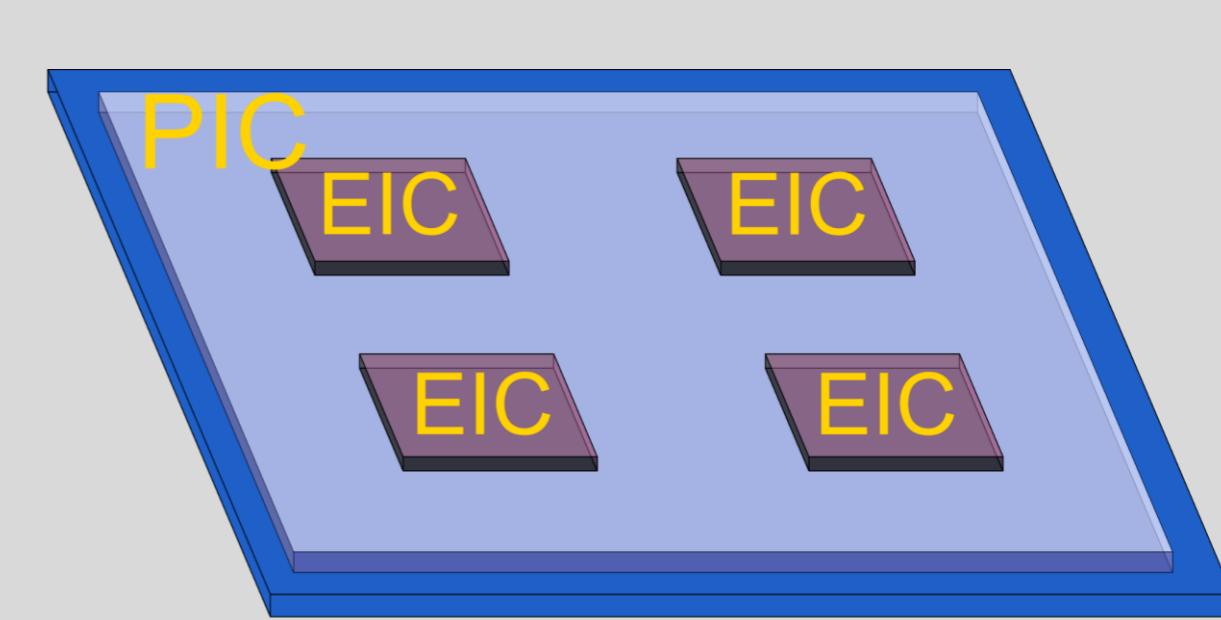
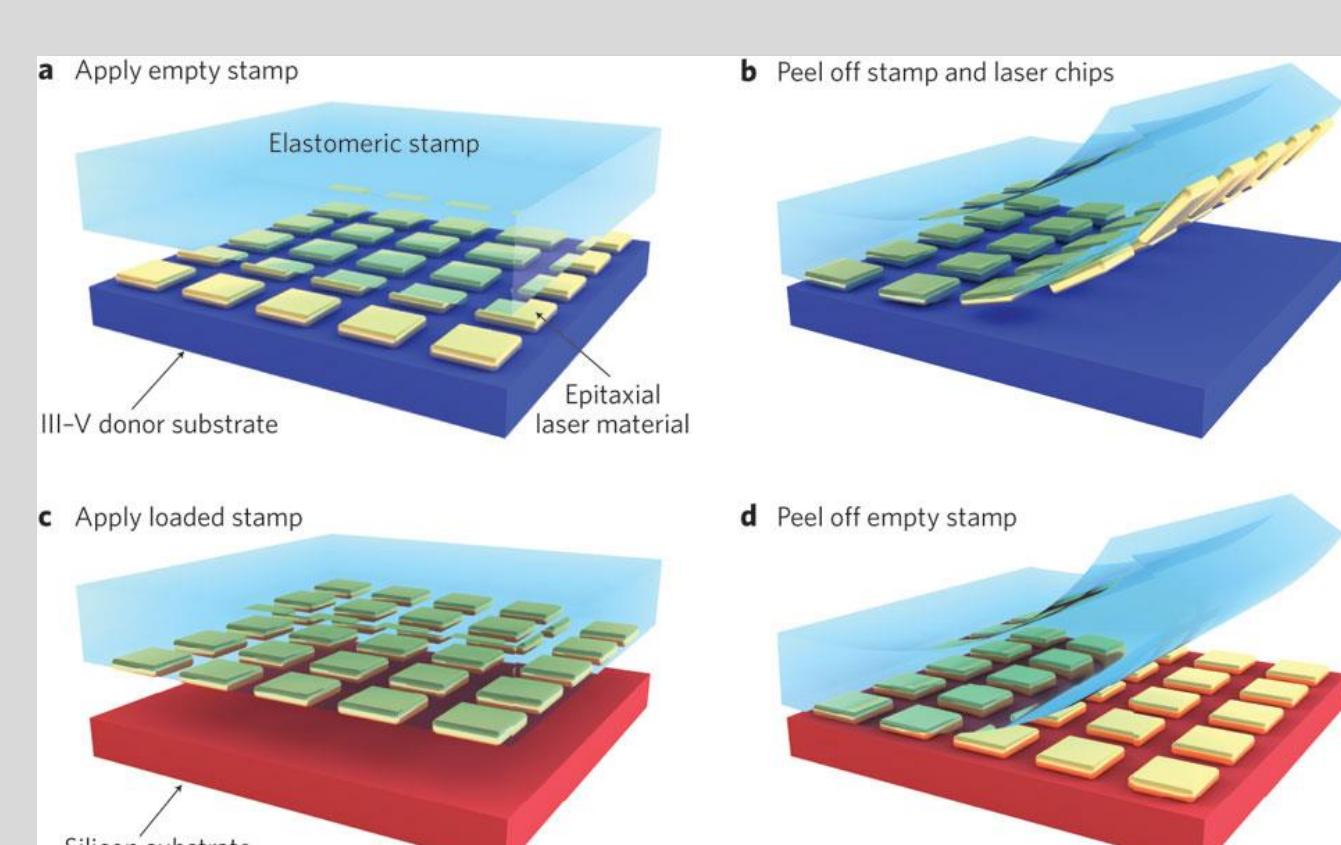


53 GBd chiplet driver amplifier

Photonic IC design



Silicon Photonics Mach-Zehnder Modulator



Integration of EIC and PIC using **micro transfer printing**

Tinus Pannier
IDLab Design



tinus.pannier@ugent.be

This project has received funding from the European Union's Horizon 2020 programme under grant agreement 825453.